

3.1.6 Class F—AC line commutated

of the supply is the phenomenon which limits the power factor of the circuit. The power factor is the ratio of the real power to the complex power. The real power is the power which is converted into heat in the load. The complex power is the power which is stored in the inductor and capacitor and is returned to the source.

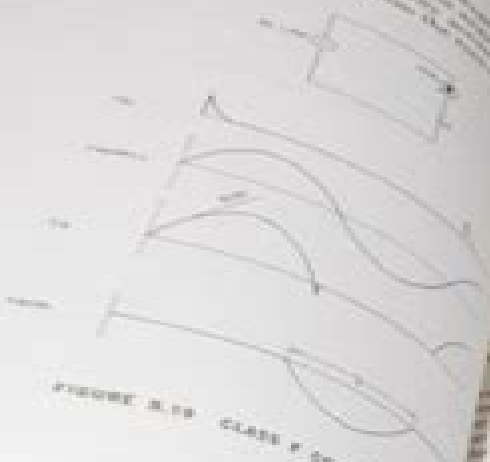


FIGURE 3.19 CLASS F COMMUTATION

RATE OF RISE OF FORWARD VOLTAGE, dv/dt

The problem of dv/dt is a serious one in many applications. A switching device subjected to this problem may become a failure as a result of the dv/dt . In this case the switching device is subjected to a voltage which is greater than the rated voltage. This is a serious problem in many applications. The switching device is subjected to a voltage which is greater than the rated voltage. This is a serious problem in many applications.

Since dv/dt is a serious problem, this phenomenon must be considered in applications in which it occurs. In many cases it is a harmful effect on the load. In other applications it is not such a problem.

The solution of dv/dt in many applications is to use a series inductor. This is a series inductor which is used to reduce the dv/dt imposed by the circuit to a value less than the rated dv/dt of the SCR being used. This is accomplished by the use of a circuit similar to those in Figure 3.20 to suppress the dv/dt of the dv/dt of the SCR.

Since circuit impedances are not usually well defined for a particular application, the values of R and C are often determined by experimental optimization. A technique described in Chapter 10 can be used to optimize the circuit design by the use of a computer which will select the circuit design to select an optimized R/C combination for a particular set of circuit operating conditions.

Another solution to the dv/dt problem is to use an SCR with higher dv/dt capability. This can be done by selecting an SCR designed specifically for high dv/dt applications, as indicated by the specification sheet. Another solution, as discussed in Chapter 1, is a manufacturing technique used to accomplish high dv/dt capability.

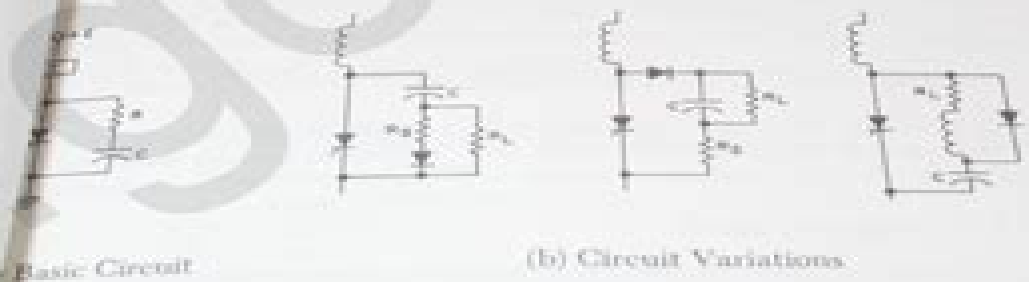


FIGURE 3.20 dv/dt SUPPRESSION CIRCUITS

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Alok Jain



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Semiconductor Power Electronics Richard G. Hoft, 2012-12-06 Semiconductors have been used widely in signal level or brain applications Since their invention in 1948 transistors have revolutionized the electronics industry in computers information processing and communications Now however semiconductors are being used more and more where considerable power is required Devices such as high power bipolar junction transistors and power field effect transistors as well as SCRs TRIACs GTOs and other semiconductor switching devices that use a p n p n regenerative effect to achieve bistable action are expanding the power handling horizons of semiconductors and finding increasing application in a wide range of products including regulated power supplies lamp dimmers motor drives pulse modulators and heat controls HVDC and electric vehicle propulsion are two additional areas of application which may have a very significant long range impact on the technology The impact of solid state devices capable of handling appreciable power levels has yet to be fully realized Since it first became available in late 1957 the SCR or silicon controlled rectifier also called the reverse blocking triode thyristor has become the most popular member of the thyristor family At present SCRs are available from a large number of manufacturers in this country and abroad SCR ratings range from less than one ampere to over three thousand amperes with voltage ratings in excess of three thousand volts *SCR Manual Sixth Edition*, 1979 **Silicon Controlled Rectifier**

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