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Dylan Blake

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web i2c module modes and features the i2c module provides the following operational modes and features master mode slave mode with byte nacking multi master mode dedicated receive and transmit buffers up to four dedicated slave address registers 1

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web the i 2 c master is byte oriented and interrupt based the number of interrupts generated is kept at a minimum by automatic handling of most incidents the software driver complexity and code size are reduced by auto triggering of operations and a special smart mode which can be enabled by the smart mode enable bit in the control b register **i2c master h file reference microchip technology** - Aug 24 2022

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web lbidirectional i2c stands for inter integrated circuit communications i2c is implemented in the picmicro by a hardware

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