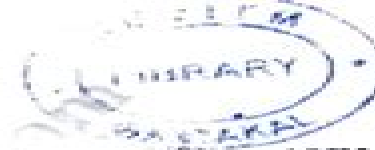


CBGS SCHEME



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18ELN14/24

First/Second Semester B.E. Degree Examination, Jan./Feb. 2023 Basic Electronics

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

1.
 - a. Explain the operation of PN junction diode under forward and reverse bias condition. (08 Marks)
 - b. Explain Zener diode as a voltage regulator. (07 Marks)
 - c. In a centre tap FWR, the forward resistance of the diode is $10\text{K}\Omega$, the load resistance is $2\text{K}\Omega$, the voltage across half the secondary winding is 220V . Calculate the ripple factor and efficiency. (05 Marks)

OR

2.
 - a. With a neat circuit diagram and wave forms, explain the working of centre tapped FWR and derive the expression for ripple factor and efficiency. (08 Marks)
 - b. Write short notes on :
 - i) Photo diode
 - ii) Light emitting diode.
 - c. For the silicon diode shown in Fig.Q2(c). Calculate the current I_F for $R_L = 4.7\text{K}\Omega$. (08 Marks)



Fig.Q2(c)

(04 Marks)

Module-2

3.
 - a. Explain the construction and operation of JFET with necessary diagrams. (08 Marks)
 - b. Draw and explain V_I characteristics of SCR. (07 Marks)
 - c. For JFET, if $I_{DSS} = 9\text{mA}$, and $V_{GS(off)} = -8\text{V(max)}$, calculate the value of I_D for $V_{GS} = 0$, and $V_{DS} = -1\text{V}$. (05 Marks)

OR

4.
 - a. With a neat diagram, explain the two transistor model of SCR. (06 Marks)
 - b. Explain the working of CMOS inverter. (06 Marks)
 - c. Explain the construction of enhancement type MOSFET. (08 Marks)

Module-3

5.
 - a. Describe the characteristics of basic op-amp. List its ideal characteristics. (08 Marks)
 - b. Derive the expression for output of inverting amplifier. (07 Marks)
 - c. Design an adder circuit using an op amp to obtain an output voltage of $V_o = -[3V_1 + 4V_2 + 5V_3]$. (05 Marks)

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